Advinno Technologies Pte Ltd

ASIC / FPGA Design Engineer
(Singapore)

Responsibilities:
- Design and development of ASIC, FPGA and Embedded based systems
- Involve in new products definition and design
- Provide technical support to customers

Requirements:
- Masters/Degree in Electrical/Electronics Engineering or equivalent
- In-depth knowledge and expert in VHDL, Verilog, C and Matlab coding
- 1-3 year experience in ASIC/SOC/FPGA/Embedded system design
- Well verse of ASIC/XILINX/ALTERA design flow

Attributes:
- Self-motivated and strong interest in engineering
- Able to work independently under tight project schedule
- Good interpersonal and communication skills

Please email with detailed resume and expected salary to hr@advinno.com